

REMARKS

Claims 1, 2-6, 8-10, 16, 18-28, and 33-36 are currently pending in the present patent application. Applicant has amended claims 1, 3, 6, 8, 16, 18, while canceling claims 2, 7, 11-15, 17, and 29-32. The claims were canceled in order to expedite the issuance of this patent application and no other inference other than expediting the issuance of this patent application should be made. Applicant preserves his rights to file a continuation and further prosecute claims 11-15 and 29-32 and disputes all accusations of non-patentability. Applicant believes that this Non-final Office Action Response has added no new matter.

Response to 35 U.S.C. §101 Rejection

The Examiner rejected claims 11-15 and 29-32 under 35 U.S.C. §101 asserting that the claimed invention is directed to non-statutory subject matter. Later in the next line of the Office Action the Examiner cites claims 11-15 and 29-36 as being directed towards non-statutory subject matter. But claims 33 and 34 were found allowable, therefore Applicant assert that the Examiner made a typographical error when claims 33-36 were cited as being rejected under 35 U.S.C §101.

In order to expedite the issuance of this patent application, Applicant has canceled claims 11-15 and 29-32. Therefore, the 35 U.S.C §101 rejection is now moot and the patent application is in condition for allowance.

Response to 35 U.S.C. §112 Rejection

The Examiner rejected claims 11-15, 23, 24, 29-32, 35, and 36 under 35 U.S.C. 112 first paragraph, as failing to comply with the enablement requirement. Applicant has cancelled claims 11-15 and 29-32 and their rejection is now moot. With regards to claims 23, 24, 27, 35 and 36,

the Examiner found that “a fourth multiplier” of “multiplying...to obtain a fourth product”, or “means for multiplying...to obtain a fourth product” is not disclosed in the specification.

The Examiner cites to Fig. 9 and page 18, lines 11-page 19, line 9 of the specification when asserting that the specification does not disclose the fourth multiple, product, or means for multiplying. Applicant directs the Examiner’s attention to page 18, lines 20-21 of the specification where the Applicant states “the circuitry 900 of FIG. 9 uses half the number of multipliers as the circuitry 800 of FIG. 8.” FIG. 8 shows four pairs 804, 806, 808, and 810 of I and Q latches, corresponding I-Q switches 812 between each of the latches, and four “one bit multipliers 814 for applying the PN chips.” FIG. 8, page 18, lines 2-7. Therefore, the Applicant has disclosed the use of four multipliers in the specification.

Therefore, Applicant submits that claims 23, 24, 35 and 36 are properly supported by the specification and that the rejection has been traversed.

Response to 35 U.S.C. §102 Rejection

The Examiner rejected claims 1 and 6 under 35 U.S.C. §102(b) as being anticipated by *Lee* patent (U.S. Pat. No 5,267,260). Applicant has amended claims 1 and 6, and believes that they are in condition for allowance along with all claims that depend from the allowable independent claims.

Response to 35 U.S.C. §103 Rejection

The Examiner rejected independent claim 16 under 35 U.S.C. §103(a) as being unpatentable over *Lee* in view of *Langberg* et al. Applicant has amended independent claim 16

and believes that claim 16 is now in condition for allowance along with the claims that depend from allowable claim 16.

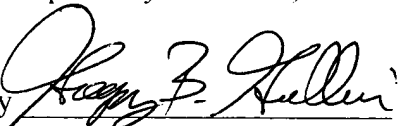
Allowable Subject Matter

Applicant acknowledges that claims 21, 22, 25, 26, 33, and 34 are in condition for allowance and claims 2-5, 7-10, and 17-20 are in condition for allowance if rewritten in independent form, and thank the Examiner for such findings.

Conclusion

In view of the foregoing discussion and the terminal disclaimer, Applicant respectfully submits that claims 1, 3-6, 8-10, 16, 18-28, and 33-36 as presented and in view of the remarks above are in a condition for allowance, for which action is earnestly solicited.

Respectfully submitted,

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